

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

1-21. (Canceled).

22. (Currently Amended) A ~~data transmission~~ method for transmitting data between a motor vehicle controller having a first processor and a test unit having a second processor, the method comprising:

transmitting first data to the motor controller to be processed at the [[a]] first processor;

determining second data as a function of the first data;

transmitting the second data to [[a]] the second processor, ~~the second data being based on the first data;~~

checking the second data in the second processor to determine if the first data may be processed in the first processor;

transmitting a check result to the first processor, the check result being a positive check result or a negative check result; and

responsive to receiving a positive check result, processing the first data at the first processor;

wherein the second data includes a cross-check sum of a segment of the first data.

23. (Previously Presented) The method according to claim 22, wherein the step of transmitting first data includes transmitting the first data to the first processor from one of a data medium drive and a third processor.

24. (Currently Amended) A ~~data transmission~~ method for transmitting data between a motor vehicle controller having a first processor and a test unit having a second processor, the method comprising:

transmitting first data to the motor controller to be processed at the [[a]] first processor;

determining second data as a function of the first data;

transmitting the second data to [[a]] the second processor, ~~the second data being based on the first data;~~

checking the second data in the second processor to determine if the first data may be processed in the first processor; [[and]]

transmitting a check result to the first processor, the check result being a positive check result or a negative check result, wherein the step of transmitting the first data includes transmitting the first data to the first processor from a data medium drive, ~~and wherein the method further comprises;~~ and

checking in the second processor an identity of a data carrier in the data medium drive;

wherein the second data includes a cross-check sum of a segment of the first data.

25. (Previously Presented) The method according to claim 22, wherein the step of transmitting the first data including transmitting the first data to the first processor from a third processor, and wherein the method further comprises:

checking in the second processor an identity of the third processor.

26. (Previously Presented) The method according to claim 22, further comprising:

checking an error-free transmission in at least one of the first processor and the second processor.

27. (Previously Amended) The method according to claim 22, wherein at least one of an utilization permission of the first data and the second data is at least one of: i) transmitting in encoded form, and ii) transmitted with an electronic signature.

28. (Previously Presented) The method according to claim 22, wherein at least one of the first data and the second data is at least one of: i) encoded with a private key of a respective processor, and ii) provided with an electronic signature.

29. (Previously Presented) The method according to claim 22, further comprising:
connecting the first processor to the second processor using a wireless connection.
30. (Previously Presented) The method according to claim 22, further comprising:
connecting the second processor to a third processor using a wireless connection.
31. (Previously Presented) The method according to claim 22, further comprising:
accessing a database in the second processor to check the second data, and
authorizing or prohibiting a use of the second data in checking the first data.
32. (Previously Presented) The method according to claim 22, further comprising:
initiating by the second processor a payment process as a function of the second data.
33. (Previously Presented) The method according to claim 22, further comprising:
allowing by the second processor a use of the first data in the first processor.
34. (Currently Amended) A ~~data transmission~~ method for transmitting data between a motor vehicle controller having a first processor and a test unit having a second processor, the method comprising:
transmitting first data to the motor controller to be processed at the [[a]] first processor;
determining second data as a function of the first data;
transmitting the second data to [[a]] the second processor, ~~the second data being based on the first data;~~
checking the second data in the second processor to determine if the first data may be processed in the first processor;
transmitting a check result to the first processor, the check result being a positive check result or a negative check result; and
storing by the second processor a use of the first data by the first processor;
wherein the second data includes a cross-check sum of a segment of the first data.
35. (Previously Presented) The method according to claim 22, further comprising:

starting a check of the first data in the first processor; and
restarting the check in the first processor if the check has not been run through completely.

36. (Previously Presented) The method according to claim 22, further comprising:
storing a program for checking at least one of the first data and the check result in a nonvolatile form in the second processor.

37. (Previously Presented) The method according to claim 22, further comprising:
deleting the first data in the first processor if a user license for the first data is not transmitted by a third processor.

38. (Previously Presented) The method according to claim 22, further comprising:
delivering a warning if the first data is not released.

39. (Previously Presented) The method according to claim 22, further comprising:
determining a first check code is determined from the first data; and
forming the second data at least in part from the first check code.

40. (Previously Presented) The method according to claim 39, further comprising:
determining a second check code from the first data; and
forming the second data at least in part from the second check code.

41. (Currently Amended) A motor vehicle device for receiving data, comprising:
a controller including a first processor;
a receiver including a second processor, the receiver being coupled to the first
processor configured to receive first data; and
a transmitter coupled to the first processor configured to transmit second data to [[a]]
the second processor, the second data being based on the first data, the second data being
checked in the second processor to determine if the first data may be processed in the first
processor, the first processor receiving via the receiver a check result from the second

processor and responsive to receiving a positive check result, the check result being a positive check result or a negative check result, processing the first data at the first processor;

wherein the second data includes a cross-check sum of a segment of the first data.

42. (Currently Amended) A controller in a motor vehicle, comprising:

a first processor residing in the controller, the first processor configured to receive first data and to transmit second data to a second processor, the second data being based on the first data, the second data being checked in the second processor to determine if the first data may be processed in the first processor, and the first processor receiving a check result from the second processor, the check result is a positive check result or negative check result, and responsive to receiving a positive check result, processing the first data at the first processor;

wherein the second data includes a cross-check sum of a segment of the first data.

43. (Currently Amended) A check processor of a motor vehicle, comprising:

a second processor configured to receive second data from a first processor in a controller of the motor vehicle, the first processor receiving first data, forming the second data from the first data, and transmitting the second data to the second processor, the second processor checking the second data to determine if the first data may be processed in the first processor and transmitting a check result to the first processor, the check result being a positive check result or a negative check result, and responsive to receiving a positive check result, processing the first data at the first processor;

wherein the second data includes a cross-check sum of a segment of the first data.

44. (Canceled).

45. (Previously Presented) The method of claim 22, wherein the checking further includes determining an admissibility of the first data.

46. (Previously Presented) The method according to claim 22, wherein the checking is executed at specifiable time intervals.

47. (Canceled).

48. (Canceled).

49. (Previously Presented) The method of claim 22, further comprising:
in response to a negative check result, deleting the first data in the first processor.

50. (New) A method for data transmission, the method comprising:
transmitting first data to a first processing unit, the first data being program data for controlling a processing unit or a device;
transmitting second data, relating to the first data, to a second processing unit, the second data in the second processing unit being checked to determine whether the first data are allowed to be used in the first processing unit;
transmitting a result of the check to the first processing unit;
transmitting a ban, on a use of the first data, from the second processing unit to the first processing unit if the first data are not allowed to be used in the first processing unit; and
transmitting a permission, if the first data are allowed to be used in the first processing unit, for using the first data from the second processing unit to the first processing unit;
wherein the permission is stored in a nonvolatile memory of the first processing unit if the first processing unit is allowed to use the first data.